

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of

HIROKI ADACHI

Serial No.: 097441,026

Filed: November 16, 1999

For: Exposure Device,
Exposure Method And
Method of Manufacturing
Semiconductor Device

Art Unit: 2814

Examiner: S. Rao



I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
the Commissioner for Patents, Washington, D.C. 20231 on

November 1, 2001

MARK J. MURPHY

Mark J. Murphy
Signature

11-1-01
Date

13A Just
M. Brunson
3/17/02

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MAY 14 2002

Technology Center 2100

Commissioner for Patents
Washington D.C. 20231

ELECTION AND AMENDMENT

Sir:

In response to the Restriction Requirement October 1, 2001, Applicant elects to prosecute
Group III: Claims 8-33 in the above-identified application. Applicant is making this election
without prejudice to later filing a divisional application on the non-elected claims.

Please also amend the above-identified application as follows:

IN THE CLAIMS:

Please add the following new claims:

03/18/2003 CPARIS 00000013 501039 09437649

01 FC:1201 168.00 CH
02 FC:1202 54.00 CH

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